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(54) Tuner

(57) An analogue single conversion tuner comprises a plurality of channels (1, 2, 3) for covering the whole of a desired reception bandwidth. Each tuner comprises an image reject mixer (18) connected to the tuner input (4) by a single tracking filter (16), for example of the single-tuned type.

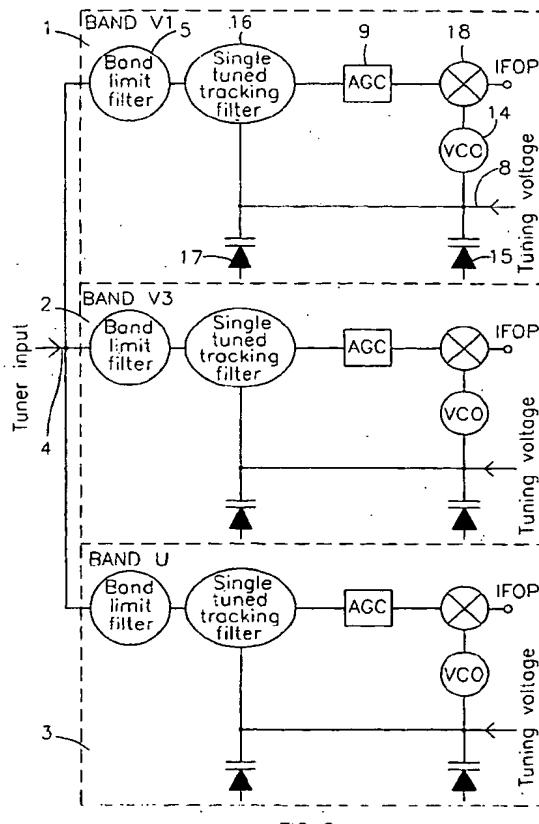


FIG. 2

Description

[0001] The present invention relates to an analogue single conversion tuner, for example for use in television tuners of television receivers and video cassette recorders.

[0002] Known analogue television tuners, for example in television receivers and video cassette recorders, are based on single conversion superheterodyne techniques in which a required radio frequency channel undergoes a single frequency conversion to an intermediate frequency before demodulation. In a typical application of such a tuner, the received bandwidth may be from about 50 to about 900 MHz or a substantial portion thereof and the intermediate frequency (IF) is typically in the region of 30 to 50 MHz, depending on which regional modulation standard is applied. Also, received channels may vary widely in received signal strength depending upon distance from a transmitter and this is referred to as the "far near effect".

[0003] Because of the use of a single conversion technique and because the IF is substantially less than the transmitted band, the image frequency is within the transmitted band at a frequency which may be occupied by another channel. Because of the far near effect, this undesired channel may be of greater amplitude than the desired channel.

[0004] Groups of channels tend to be transmitted in clusters which occupy a smaller part of the received bandwidth. Although provision may be made for spatially separating such channels so that they are much less likely to interfere with each other, this is not always the case. In any case, the far near effect may result in strong potentially interfering channels occupying neighbouring frequencies to desired channels.

[0005] It is thus a requirement for such tuners to provide sufficient image rejection and cross modulation and intermodulation protection from interfering signals. In known television tuners, this is achieved by pre-filtering the received channels so that substantially only the desired channel is supplied to the frequency changing mixer. Provided potentially interfering signals are sufficiently attenuated, the desired channel can be selected and demodulated with acceptable performance. Such pre-filtering is provided by a plurality of filters between the mixer and the input to the tuner. However, because the tuner must be capable of tuning so as to select different channels, the filters must also be capable of tuning and must track the frequency of the local oscillator whose signal is also supplied to the mixer.

[0006] In practice, using low cost varactor diodes for performing the tuning function, a tuning range of only one or maybe 1.2 octaves can be achieved. It is therefore common practice to divide a tuner into three dependent channels for providing adequate frequency coverage of the whole received bandwidth within which desired channels may occur. Each of these independent channels requires its own tracking filters.

[0007] The local oscillator frequency also lies within the received bandwidth and it is therefore necessary to suppress re-radiation of the local oscillator signal from the tuner so as to prevent interference with other tuners.

5 This is generally achieved by a further tracking filter for suppressing the local oscillator re-radiation.

[0008] A typical known type of single conversion television tuner thus contains three independent channels or bands, each of which has two sets of tracking filters
10 which must provide sufficiently high Q to attenuate neighbouring potentially interfering channels (in the case of image filtering) and little or no attenuation to the desired channel (in the case of the local oscillator suppression filter). All of these filters must accurately track
15 the frequency of the local oscillator.

[0009] The required tracking accuracy may be achieved by alignment during production of the television tuner or by some form of automatic alignment algorithm within the tuner. Production alignment requires
20 an iterative adjustment during production whereas automatic alignment requires an iterative adjustment during use of a tuner. Both techniques involve significant costs.

[0010] Figure 1 of the accompanying drawings illustrates the architecture of a typical known type of analogue television tuner, for example as disclosed in. "Colour Television" by Hutson, Shepherd and Brice, McGraw Hill, p104, ISBN 0-07-084199-3. The tuner comprises three channels or bands 1, 2, 3 of substantially identical
25 arrangement or architecture but differing in the frequency ranges which they cover. The first channel 1 is designed to cover the band V1, the second channel 2 is designed to cover the band V3 and the third channel 3 is designed to cover the band U. Because the channels
30 are substantially identical, only channel 1 will be described, channels 2 and 3 having the same structure. A tuner input 4 is connected to the inputs of the channels 1 to 3. In channel 1, the input is connected to a band limit filter 5 which limits the incoming bandwidth to the
35 range covered by the channel 1. The band limit filter 5 can therefore have a fixed frequency response and, in particular, is not required to track the local oscillator frequency.

[0011] The output of the filter 5 is supplied to a single-tuned tracking filter 6 whose frequency is controlled by a varactor diode 7 in response to a tuning voltage supplied to an input 8. The filter 6 tracks the local oscillator frequency so as to suppress re-radiation of the local oscillator signal without substantially attenuating the desired channel.

[0012] The output of the filter 6 is supplied via an automatic gain control circuit 9 to a double-tuned tracking filter 10 which is tuned by varactor diodes 11 and 12 in accordance with the tuning voltage at the input 8. The
55 filter 10 is an image reject filter for rejecting the image channel and has a bandwidth which is centred on the frequency of the desired channel with a -3dB bandwidth marginally greater than the bandwidth of the desired

channel. The output of the filter 10 is supplied to a mixer 13 which also receives the local oscillator signal from a local oscillator 14 which is tuned by a varactor diode 15 controlled by the control voltage at the input 8. The local oscillator frequency is generally equal to the radio frequency of the desired channel plus the intermediate frequency. The capacitances of the varactor diodes 7, 11, 12 and 15 vary in accordance with the tuning voltage and change the resonant frequencies of the networks with which they are associated. As is known, the resonant networks are aligned during manufacture such that, over the tuning voltage range, the centre frequencies of the various networks maintain the correct relative values or alignment.

[0013] This type of tuner has several disadvantages. For example, the radio frequency path between the input and the mixer of each channel or band 1, 2, 3 contains at least two tracking filters. The tracking filters must be aligned during manufacture or a dynamic alignment algorithm must be provided to maintain alignment during operation. Both techniques involve substantial cost. Also, in order to achieve the appropriate alignment, the varactor diodes 7, 11, 12, 15 must be well-matched. This increases component cost and causes difficulties in selecting a replacement diode if one of the diodes fails during use.

[0014] According to a first aspect of the invention, there is provided an analogue single conversion tuner comprising an image reject mixer, characterised by a single tracking filter between the mixer and a tuner input.

[0015] The single tracking filter may be a bandpass filter.

[0016] The single tracking filter may be single-tuned.

[0017] An automatic gain control circuit may be disposed between the mixer and the single tracking filter.

[0018] A fixed-tuned band limit filter may be disposed between the single tracking filter and the tuner input.

[0019] According to a second aspect of the invention, there is provided a television tuner comprising at least one tuner according to the first aspect of the invention.

[0020] According to a third aspect of the invention, there is provided a television receiver comprising a television tuner according to the second aspect of the invention.

[0021] According to a fourth aspect of the invention, there is provided a video cassette recorder comprising a television tuner according to the second aspect of the invention.

[0022] It is thus possible to provide a tuner of simplified construction and reduced cost. By reducing the required selectivity ahead of the mixer, a relatively coarse production alignment may be used, thus reducing the cost of production alignment and removing the need for automatic alignment during use, which also reduces the cost. No image reject filtering is required and a single relatively low Q tracking filter is provided ahead of the mixer. Provided the mixer has a sufficiently wide dynamic range, acceptable performance can be achieved with

a significant reduction in cost and increased ease of manufacture as compared with known tuner architectures.

[0023] The invention will be further described, by way of example, with reference to the accompanying drawings, in which:

Figure 1 is a block schematic diagram of a known type of analogue single conversion television tuner; and

Figure 2 is a block schematic diagram of an analogue single conversion television tuner constituting an embodiment of the invention.

[0024] Like reference numerals refer to like parts in the drawings.

[0025] The tuner of Figure 2 comprises the three bands or channels 1, 2, 3 as in the tuner of Figure 1. Again, as the three channels are substantially identical other than for their frequency coverage, only the channel 1 will be described.

[0026] The channel 1 shown in Figure 2 differs from that shown in Figure 1 in that the single-tuned image reject filter 6 and the associated diode are omitted, as are the double-tuned tracking filter 10 and associated diodes 11 and 12. Instead, a single-tuned tracking filter 16 is disposed between the band limit filter 5 and the AGC circuit 9. The filter 16 is aligned to the desired channel and is required to track with the local oscillator frequency. However, because the filter 16 is of relatively low Q type, the alignment can be relatively coarse because the roll-off in frequency response is relatively shallow so that misalignments during tracking do not significantly affect the performance of the tuner.

[0027] The channel 1 shown in Figure 2 also differs from that shown in Figure 1 in that the mixer 13 is replaced by an image reject mixer 18 whose operation is such as to reject the image channel and so substantially prevent interference therewith. The use of an image reject mixer thus allows image reject filtering to be omitted. Image reject mixers are known and a typical example is disclosed in J. C. Rudell, J. J. Ou, T. Cho, G. Chien, F. Brianti, J. Weldon and P. R. Gray, "A1.9 Ghz Wide-Band IF Double Conversion CMOS Integrated Receiver for Cordless Telephone Applications", J. Solid-State Circuits, Vol 32 No. 12, December 1997, the contents of which are incorporated herein by reference.

[0028] By using an image reject mixer 18 having a very wide dynamic range, the effect of products of relatively high amplitude interference channels passing to the mixer 18 because of the reduced selectivity ahead of the mixer can be reduced to a sufficient extent to provide acceptable performance. Thus, in addition to being of the image reject type, the mixer is required to produce low levels of intermodulation and cross modulation effects even in the presence of relatively high potentially interfering signals.

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[0029] This arrangement allows the cost of the tuner to be significantly reduced compared with known types of tuner, for example as shown in Figure 1. For example, a significant number of active devices may be omitted from the tuner, thus reducing its complexity. Also, alignment requirements are substantially reduced because only a single tracking filter is required to be aligned coarsely with the local oscillator. Also, it is not necessary for the varactor diodes 15 and 17 to be accurately matched, thus reducing the cost of the components and reducing the difficulty in effecting repairs if a varactor diode needs to be replaced.

Claims

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1. An analogue single conversion tuner comprising an image reject mixer (18), **characterised by** a single tracking filter (16, 17) between the mixer (18) and a tuner input (4). 20
2. A tuner as claimed in claim 1, **characterised in that** the single tracking filter (16, 17) is a bandpass filter.
3. A tuner as claimed in claim 1 or 2, **characterised in that** the single tracking filter (16, 17) is single-tuned. 25
4. A tuner as claimed in any one of the preceding claims, **characterised in that** an automatic gain control circuit (9) is disposed between the mixer (18) and the single tracking filter (16, 17). 30
5. A tuner as claimed in any one of the preceding claims, **characterised in that** a fixed-tuned band limit filter (5) is disposed between the single tracking filter (16, 17) and the tuner input (4). 35
6. A television tuner **characterised by** at least one tuner (1, 2, 3) as claimed in any one of the preceding claims. 40
7. A television receiver **characterised by** a television tuner as claimed in claim 6. 45
8. A video cassette recorder **characterised by** a television tuner as claimed in claim 6.

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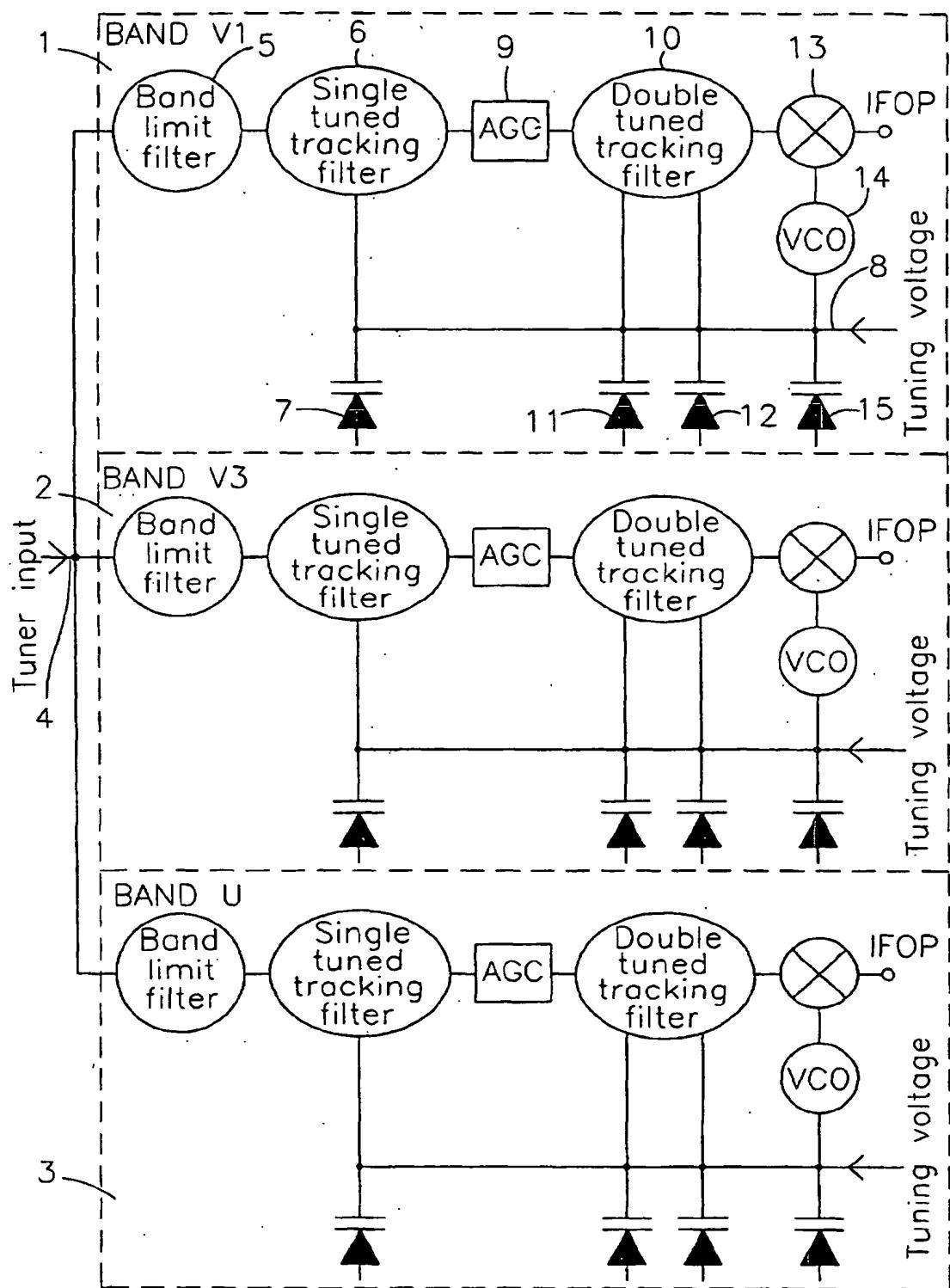


FIG. 1

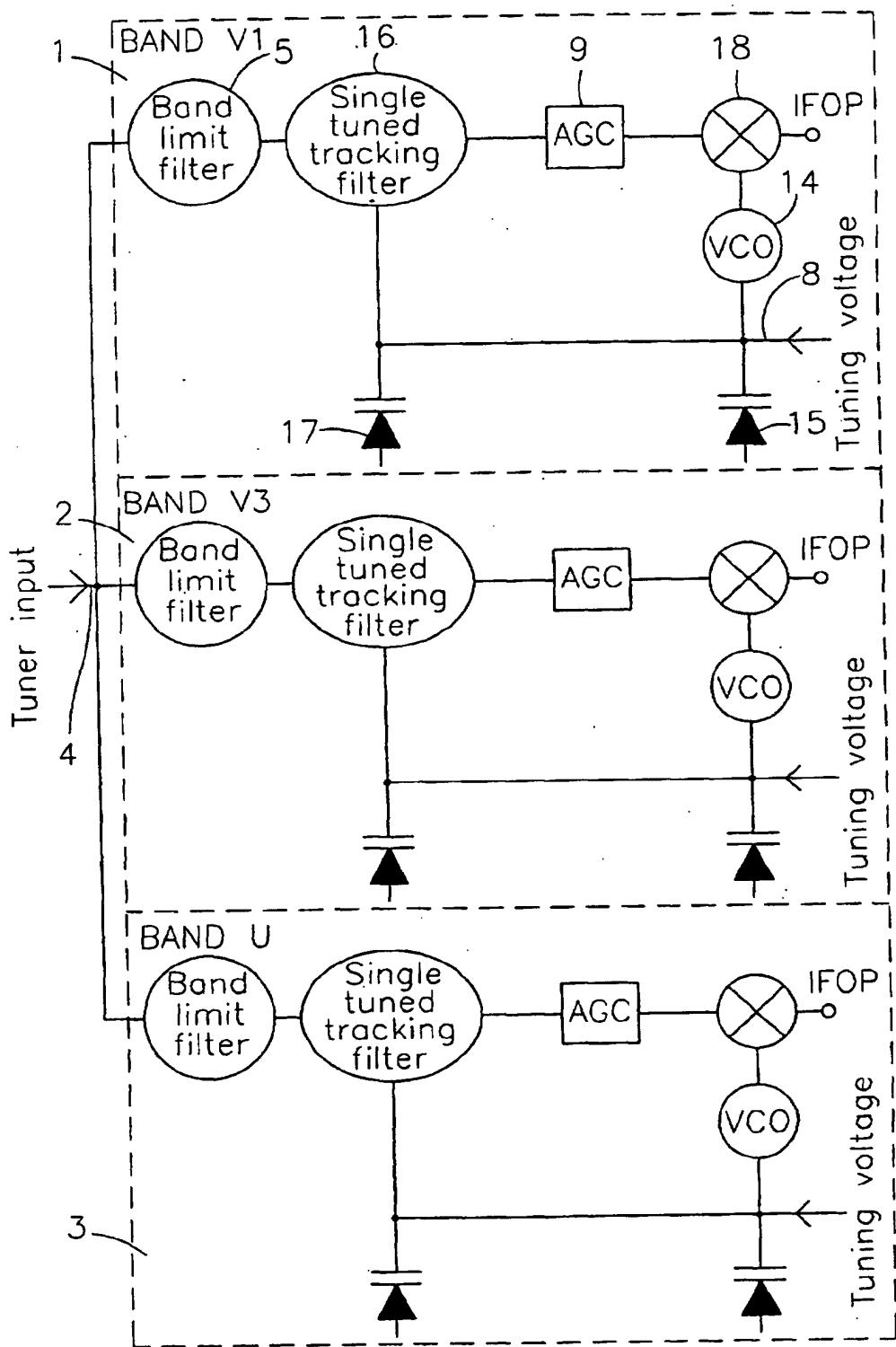


FIG 2

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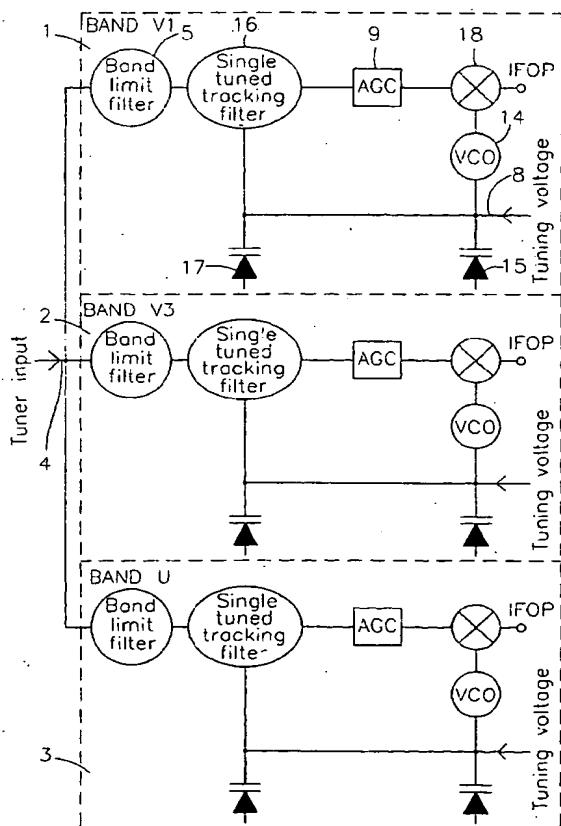


FIG. 2



European Patent
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EUROPEAN SEARCH REPORT

Application Number
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Place of search THE HAGUE	Date of completion of the search 3 December 2003	Examiner Peeters, M	
CATEGORY OF CITED DOCUMENTS		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	
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